

Fig. 1

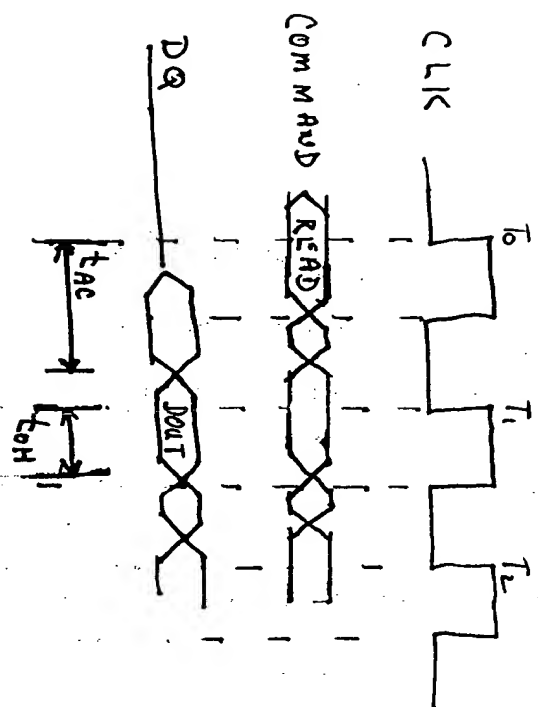


Fig. 2

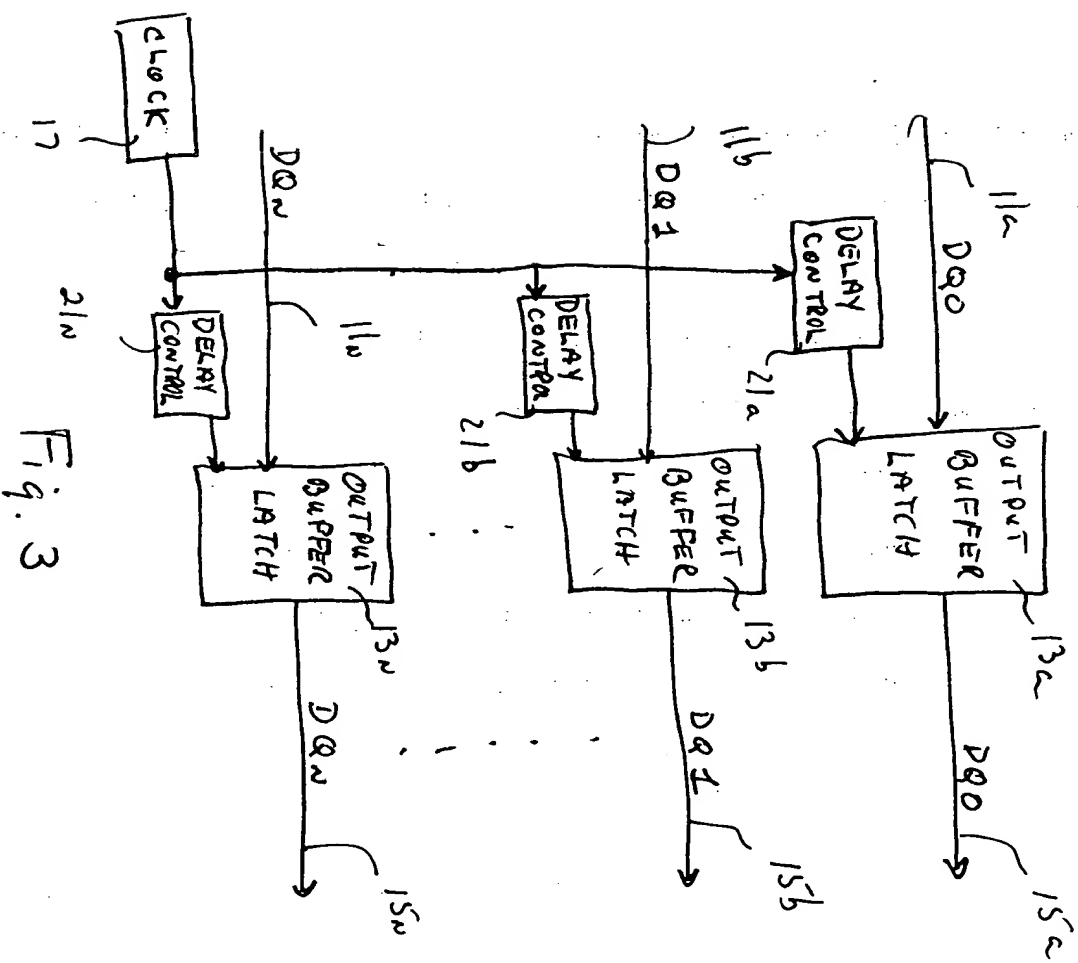


Fig. 3

FIG. 3 is a block diagram of a multi-bit data path in accordance with the present invention.

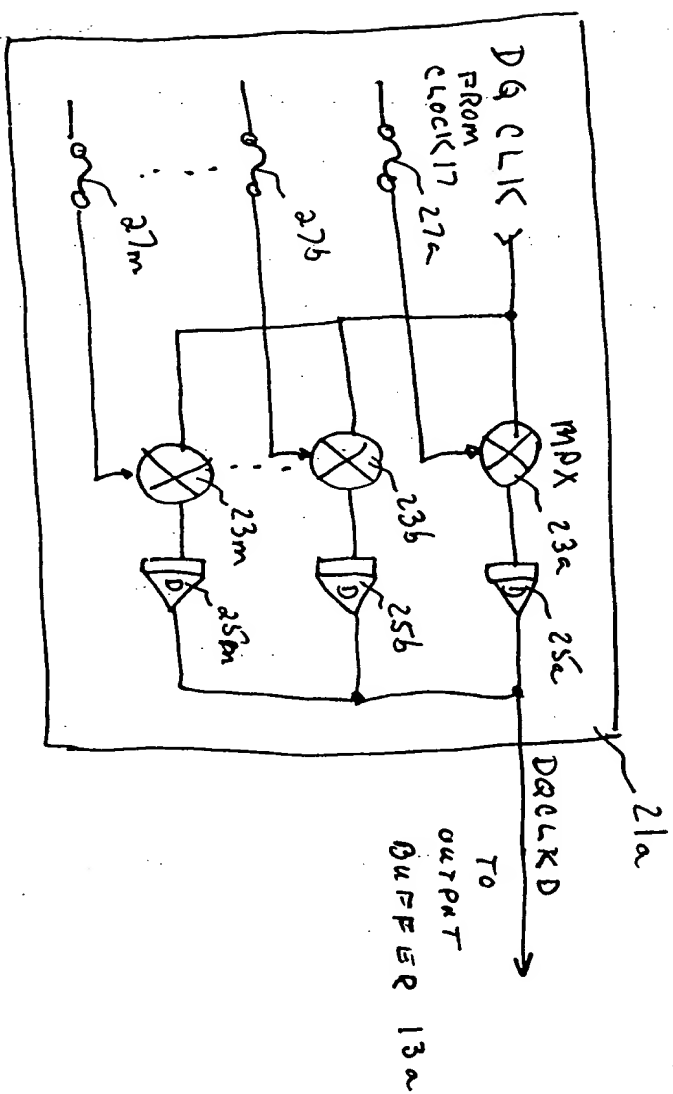


Fig. 4

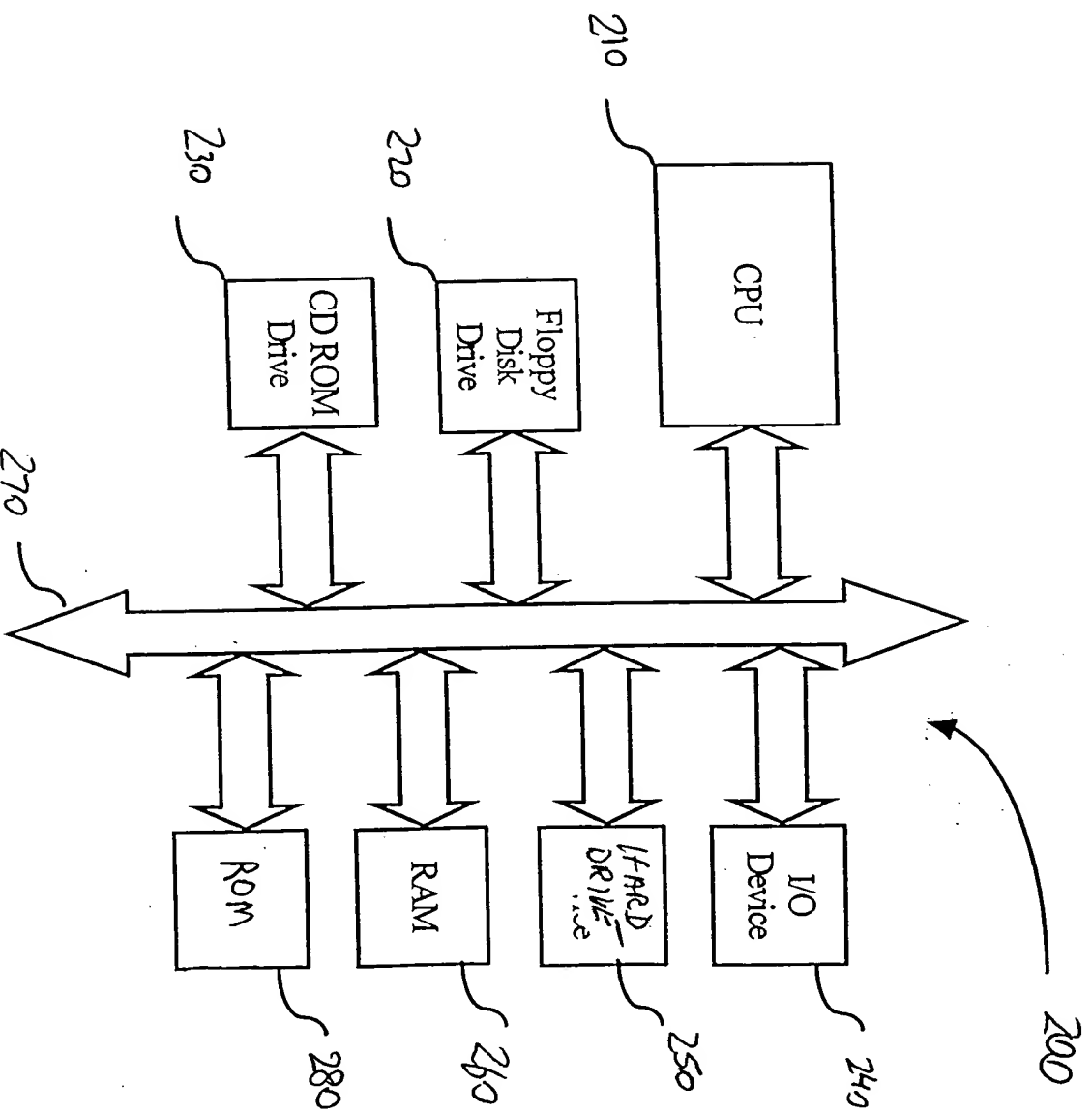


Fig. 5